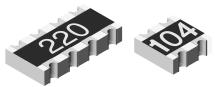
## Vishay



# **Thick Film Resistor Array**



CRA06P Thick Film resistor array is constructed on a high grade ceramic body with concave terminations. A small package enables the design of high density circuits. The single component reduces board space, component counts and assembly costs.

### **FEATURES**



- · Concave terminal array with square corners
- · 4 and 8 terminal package with isolated resistors
- Wide ohmic range: 10R to 1M0
- Lead (Pb)-free solder contacts on Ni barrier layer
- Pure tin plating provides compatibility with lead (Pb)-free and lead containing soldering processes
- Compatible with "Restriction of the use of Hazardous Substances" (RoHS) directive 2002/95/EC (issue 2004)

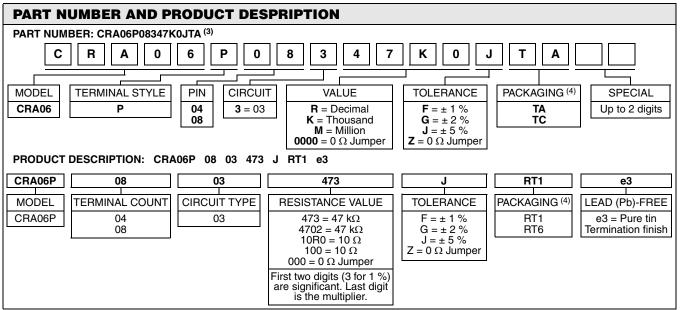
STANDARD ELECTRICAL SPECIFICATIONS								
MODEL	CIRCUIT	POWER RATING  P <sub>70 °C</sub> W	LIMITING ELEMENT VOLTAGE MAX. $V_{\cong}$	TEMPERATURE COEFFICIENT ppm/K	TOLERANCE %	$\begin{array}{c} \textbf{RESISTANCE} \\ \textbf{RANGE} \\ \Omega \end{array}$	E-SERIES	
CRA06P	03	0.063	50	± 100 ± 200	± 1 ± 2; ± 5	10R - 1M0	24 + 96 24	
		Zero-Ohm-Resistor: $R_{\text{max.}} = 50 \text{ m}\Omega$ , $I_{\text{max.}} = 1 \text{ A}$						

TECHNICAL SPECIFICATIONS						
PARAMETER	UNIT	CRA06P 03 CIRCUIT				
Rated Dissipation at 70 °C (2)	W per element	0.063				
Limiting Element Voltage (1)	V≅	50				
Insulation Voltage (1 min)	V <sub>dc/ac peak</sub>	100				
Category Temperature Range	°C	- 55 to + 155				
Insulation Resistance	Ω	> 10 <sup>9</sup>				

### Notes

(1) Rated voltage:  $\sqrt{P \times R}$ 

<sup>(2)</sup> The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature of 155 °C is not exceeded.



### Notes

(3) Preferred way for ordering products is by use of the PART NUMBER

(4) Please refer to the table PACKAGING, see next page

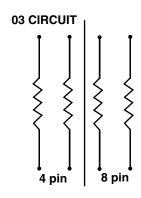
Document Number: 31047 Revision: 13-Oct-08

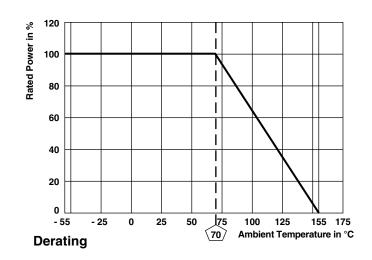


# Thick Film Resistor Array

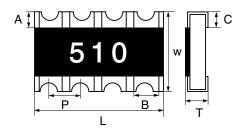
PACKAGING							
		DIAMETER	PITCH	PIECES /REEL	PACKAGING CODE PAPER TAPE		
MODEL	TAPE WIDTH						
					PART NUMBER	PRODUCT DESCRIPTION	
CRA06P	8 mm	180 mm/7"	4 mm	5000	TA	RT1	
		330 mm/13"	4 mm	20 000	TC	RT6	

## **CIRCUIT**

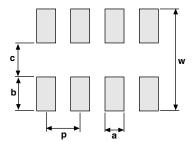




## **DIMENSIONS**



PIN	DIMENSIONS [in millimeters]							
NO#	L	Α	В	C	Р	Т	W	
4	1.60	0.30	0.40	0.40	0.80	0.60	1.60	
8	3.20	0.30	0.40	0.40	0.80	0.60	1.60	
Tol.	± 0.20	± 0.20	± 0.15	± 0.20	ı	± 0.10	± 0.15	



SOLDER PAD DIMENSIONS [in millimeters]						
	c w p a b					
WAVE	0.8	2.6	0.8	0.4	0.9	

## Thick Film Resistor Array



TEST PROCEDURES AND REQUIREMENTS						
EN 60115-1						
TEST	COMPITIONS OF TEST	REQUIREMENTS PERMISSIBLE CHANGE ( $\triangle R/R$ ) $^{(1)}$				
(clause)	CONDITIONS OF TEST	STABILITY CLASS 1 OR BETTER	STABILITY CLASS 2 OR BETTER			
	Stability for product types:  CRA06P	10 Ω to 1 MΩ	10 Ω to 1 MΩ			
Resistance (4.5)	-	± 1 %	± 2 %; ± 5 %			
Temperature coefficient (4.8.4.2)	20/- 55/20 °C and 20/125/20 °C	± 100 ppm/K	± 200 ppm/K			
Overload (4.13)	$U = 2.5 \times (P_{70} \times R)^{1/2}$ $\leq 2 \times U_{\text{max.}}; 0.5 \text{ s}$	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)			
Solderability (4.17.5) (2)	Aging 4 h at 155 °C, dryheat Solder bath method; 235 °C; 2 s Visual examination	Good tinning (≥ 95 % covered) no visible damage				
Resistance to soldering heat (4.18.2)	Solder bath method; (260 ± 5) °C; (10 ± 1) s	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)			
Rapid change of temperature (4.19)	30 min at LCT = - 55 °C; 30 min at UCT = 125 °C; 5 cycles	± (0.25 % R + 0.05 Ω)	± (0.5 % R + 0.05 Ω)			
Damp heat, steady state (4.24)	(40 ± 2) °C; 56 days; (93 ± 3) % RH	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)			
Climatic sequence (4.23)	16 h at UCT = 125 °C; 1 cycle at 55 °C; 2 h at LCT = -55 °C; 1 h/1 kPa at 15 °C to 35 °C; 5 cycles at 55 °C $U = (P_{70} \times R)^{1/2}$ $U = U_{\text{max.}}$ ; whichever is less severe	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)			
Endurance at 70 °C (4.25.1)	$U = (P_{70} \times R)^{1/2}$ $U = U_{\text{max.}}$ ; whichever is less severe 1.5 h ON; 0.5 h OFF; 70 °C; 1000 h	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)			
Extended endurance (4.25.1.8)	Duration extended to 8000 h	± (2 % R + 0.1 Ω)	± (4 % R + 0.1 Ω)			
Endurance at upper category temperature (4.25.3)	UCT = 125 °C; 1000 h	± (1 % R + 0.05 Ω)	± (2 % R + 0.1 Ω)			

### **Notes**

(1) Figures are given for a single element

 $^{(2)}$  Solderability is specified for 2 years after production or requalification. Permitted storage time is 20 years

## **APPLICABLE SPECIFICATIONS**

EN 60115-1 Generic Specification
 EN 140400 Sectional Specification
 EN 140401-802 Detail Specification

• IEC 60068-2-X Variety of environmental test procedures

EIA 481 Packaging of SMD components

Document Number: 31047 Revision: 13-Oct-08



Vishay

## **Disclaimer**

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.

Revision: 18-Jul-08

Document Number: 91000 www.vishay.com